

Vishay Siliconix

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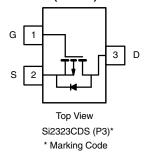
COMPLIANT HALOGEN

FREE

P-Channel 20 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)		
	0.039 at V _{GS} = -4.5 V	-6 ^e			
-20	0.050 at V _{GS} = -2.5 V	-5.8	9 nC		
	0.063 at V _{GS} = -1.8 V	-5.1			



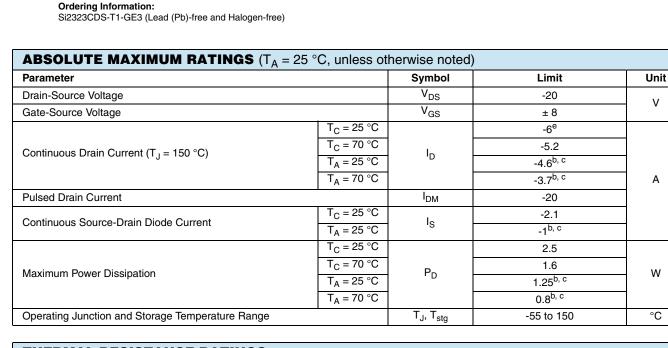


FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Load Switch
- PA Switch
- **DC/DC** Converters



THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	75	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	50	C/W	

Notes:

a. Based on T_C = 25 °C.

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b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 166 °C/W.

e. Package limited.

For technical questions, contact: pmostechsupport@vishav.com

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MOSFET SPECIFICATIONS	(T _J = 25 °C	, unless otherwise noted)					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					_		
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_{D} = -250 \mu A$	-20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L _ 250 HA		-14		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA		2.4			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	-0.4		-1	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	lace	$V_{DS} = -20 V, V_{GS} = 0 V$			-1		
Zero Gale Vollage Diam Current	IDSS	V_{DS} = -20 V, V_{GS} = 0 V, T_{J} = 55 °C			-10	- μΑ	
On-State Drain Current ^a	I _{D(on)}	V_{DS} \leq -5 V, V_{GS} = -4.5 V	-20			Α	
		V _{GS} = -4.5 V, I _D = -4.6 A		0.032	0.039		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = -2.5 V, I _D = -4.1 A		0.041	0.050	Ω	
		V _{GS} = -1.8 V, I _D = -3.6 A		0.050	0.063		
Forward Transconductance ^a	9 _{fs}	V _{DS} = -5 V, I _D = -4.6 A		20		S	
Dynamic ^b	I						
Input Capacitance	C _{iss}			1090		pF	
Output Capacitance	C _{oss}	V_{DS} = -10 V, V_{GS} = 0 V, f = 1 MHz		155			
Reverse Transfer Capacitance	C _{rss}			135			
Tatal Cata Obarra		V_{DS} = -10 V, V_{GS} = -4.5 V, I_{D} = -4.6 A		16	25		
Total Gate Charge	Qg		9.3	15			
Gate-Source Charge	Q _{gs}	V_{DS} = -10 V, V_{GS} = -2.5 V, I_{D} = -4.6 A		2.5		nC	
Gate-Drain Charge	Q _{gd}			3.2			
Gate Resistance	R _g	f = 1 MHz	0.8	4.1	8.2	Ω	
Turn-On Delay Time	t _{d(on)}			15	23		
Rise Time	t _r	V_{DD} = -10 V, R_L = 2.7 Ω		23	35	ns	
Turn-Off Delay Time	t _{d(off)}	$\rm I_D$ = -3.7 A, $\rm V_{GEN}$ = -4.5 V, $\rm R_g$ = 1 Ω		40	60		
Fall Time	t _f			12	20		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			-2.1	A	
Pulse Diode Forward Current ^a	I _{SM}			l l	-20		
Body Diode Voltage	V _{SD}	I _S = -3.7 A		-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			30	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			20	40	nC	
Reverse Recovery Fall Time	t _a	$I_F = -3.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		17			
-	-					ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

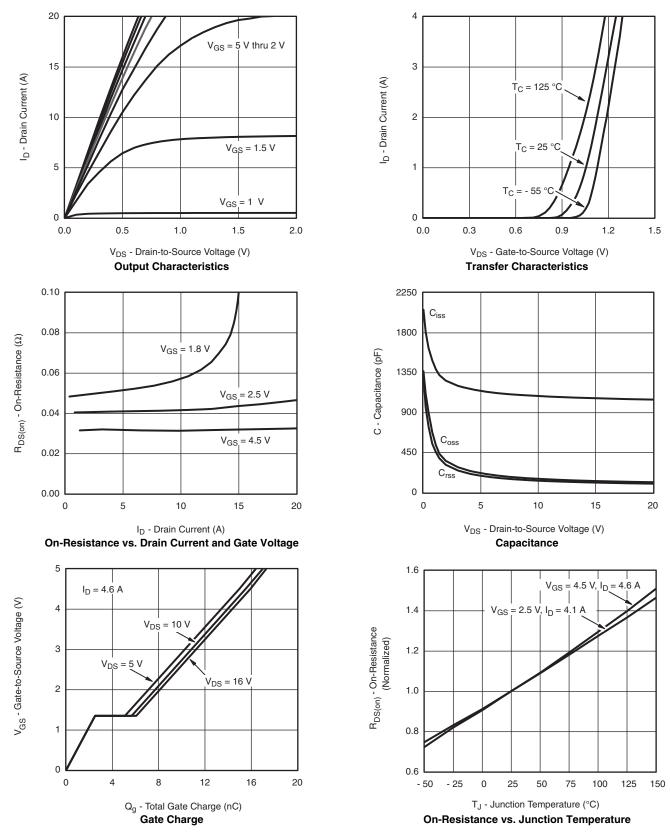
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si2323CDS Vishay Siliconix

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Document Number: 65700 S13-2081-Rev. B, 30-Sep-13 For technical questions, contact: pmostechsupport@vishay.com

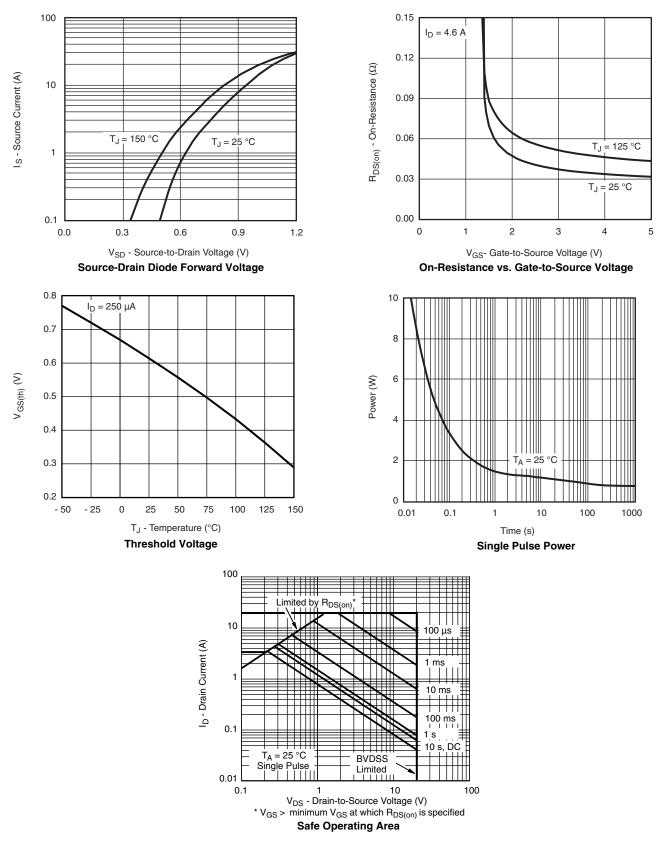
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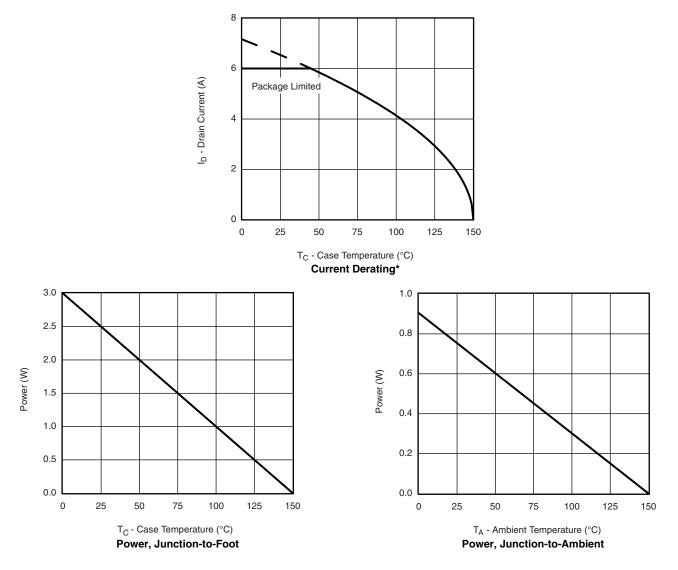


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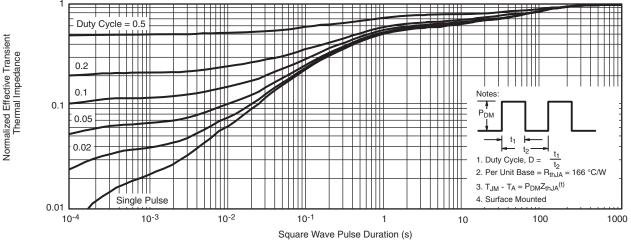


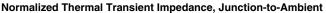
* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

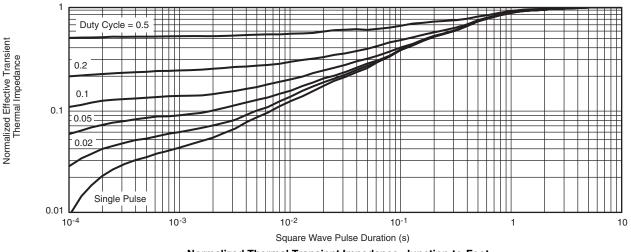


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65700.

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Package Information

Vishay Siliconix

SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



Application Note 826

Vishay Siliconix

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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